

Received

SEP 05 2002

Technology Center 2100

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Timothy J. VAN HOOK et al.

Appl. No.: 09/662,832

Filed: September 15, 2000

For: **Alignment and Ordering of Vector
Elements for Single Instruction
Multiple Data Processing**

Confirmation No. ~~2552~~ ²¹⁸³

Art Unit: ~~2552~~ ²¹⁸³

Examiner: Pan, D.

Atty. Docket: 1778.0100002

Received

SEP 06 2002

Technology Center 2100

Letter Re-Submitting Non-U.S. Patent Documents

Commissioner for Patents
Washington, D.C. 20231

**HANDCARRY TO:
Mr. Daniel Pan, CP 2, 2A05
4th Floor Reception**

Sir:

In accordance with a request from Examiner Daniel Pan, on August 28, 2002, Applicants hereby re-submit copies of the non-U.S. Patent documents previously filed with an Information Disclosure Statement on May 22, 2002.

Applicants also hereby re-submit a copy of the form PTO-1449 listing all of the documents filed on May 22, 2002. Per a telephone conversation and agreement with Examiner Pan on September 5, 2002, the cited U.S. Patent documents are not being re-submitted. The re-submitted documents only include copies of the documents identified on the PTO-1449 with document reference numbers AJ1-AR1, AN2-AR2, AN3-AR3, AN4-AR4, AN5-AR5, AN6-AR6, AN7-AR7 and AN8-AR8.

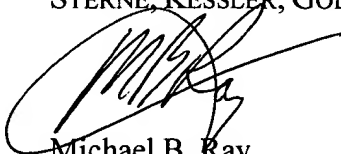
Applicants note that the copy of the document No. AO4 submitted herewith is missing pages A-122 and A-172.

It is respectfully requested that the attached postcard be stamped with the date of re-submission of the above listed documents, and that it be returned to our courier. In the event that extensions of time are necessary to prevent abandonment of this patent application, then such extensions of time are hereby petitioned.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.



Michael B. Ray
Attorney for Applicants
Registration No. 33,997

Date: 9/5/02

1100 New York Avenue, N.W.
Washington, D.C. 20005-3934
(202) 371-2600

Applicant: VAN HOOK et al.
Due Date:
Art Unit: 2183
Examiner: Pan, D.
Docket: 1778.0100002
Atty: MBR/BAM/mjg
Application No.: 09/662,832
Filed: September 15, 2000
For: Alignment and Ordering of Vector Elements for Single Instruction Multiple Data Processing

When receipt stamp is placed hereon, the USPTO acknowledges receipt of the following documents:

1. SKGF Cover Letter
2. PTO Fee Transmittal Form (PTO/SB/17);
3. Request for Continued Examination (RCE) Transmittal (PTO/SB/30);
4. Preliminary Amendment Under 37 C.F.R. § 1.114
5. Information Disclosure Statement;
6. A list of the cited documents on Form PTO-1449 (8 pages);
7. A copy of the (88) documents cited on Form PTO-1449;
8. Return postcard; and
9. Our Check No. 35238 for \$ 740.00 to cover RCE application filing.



Box: RCE

Please Date Stamp And Return To Our Courier

SKGF_DC1:8663.1

STERNE, KESSLER, GOLDSTEIN & FOX PLLC
ATTORNEYS AT LAW
GOVERNMENT FEES ACCT.
 1100 NEW YORK AVE. NW, SUITE 600
 WASHINGTON, D.C. 20005

REMITTANCE ADVICE			

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FORM PTO-1449

INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO.
1778.0100002 (0055.20US)APPLICATION NO.
09/662,832APPLICANT
Van Hook *et al.*FILING DATE
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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	AA1	3,654,621	04/1972	Bock <i>et al.</i>	340	172.5
	AB1	3,916,388	10/1975	Shimp <i>et al.</i>	340	172.5
	AC1	4,023,023	05/1977	Bourrez <i>et al.</i>	235	152
	AD1	4,109,310	08/1978	England <i>et al.</i>	364	200
	AE1	4,128,880	12/1978	Cray, Jr.	364	200
	AF1	4,130,880	12/1978	Nutter	364	900
	AG1	4,219,874	08/1980	Gusev <i>et al.</i>	364	200
	AH1	4,317,170	02/1982	Wada <i>et al.</i>	364	200
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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION
	AJ1	WO 97/07450 A1	02/1997	PCT	G06F	7/00	N/A
	AK1	WO 97/08608 A1	03/1997	PCT	G06F	7/00	N/A
	AL1	0 681 236 A1	11/1995	EP	G06F	9/302	N/A
	AM1	0 681 236 B1	11/2000	EP	G06F	9/302	N/A

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

AN	1	An Introduction to Thumb™, Version 2.0, Advanced RISC Machines, 29 pages (March 1995).
AO	1	Bier, J., "DSP16xxx Targets Communications Apps," <i>Microprocessor Report</i> , MicroDesign Resources, pp. 11-15 (September 15, 1997).
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AR	1	Diefendorff, K. and Allend, M., "Organization of the Motorola 88110: A Superscalar RISC Microprocessor," 12 pages, Source and date unknown.

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	AB2	4,507,731	03/1985	Morrison	364	200	
	AC2	4,520,439	05/1985	Liepa	364	200	
	AD2	4,583,199	04/1986	Boothroyd <i>et al.</i>	364	900	
	AE2	4,773,006	09/1988	Kinoshita <i>et al.</i>	364	200	
	AF2	4,814,976	03/1989	Hansen <i>et al.</i>	364	200	
	AG2	5,150,290	09/1992	Hunt	364	736	
	AH2	5,418,915	05/1995	Matuda <i>et al.</i>	395	325	
	AI2	5,513,366	04/1996	Agarwal <i>et al.</i>	395	800	

FOREIGN PATENT DOCUMENTS

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	AJ2						Yes No
	AK2						Yes No
	AL2						Yes No
	AM2						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AN	2	Fiegel, C.P., "TI Introduces Four-Processor DSP Chip," <i>Microprocessor Report</i> , MicroDesign Resources, pp. 22-25 (March 28, 1994).
	AO	2	Gwennap, L., "Digital, MIPS Add Multimedia Extensions," <i>Microprocessor Report</i> , MicroDesign Resources, Vol. 10, No. 15, pp. 1-5 (November 18, 1996).
	AP	2	Gwennap, L., "New Multimedia Chips to Enter the Fray," <i>Microprocessor Report</i> , MicroDesign Resources, Vol. 10, No. 13, 1 page (October 1996).
	AQ	2	Hansen, C., "Architecture of a Broadband Mediaprocessor," <i>COMPCON 96</i> , IEE, pp. 1-8 (February 25-29, 1996).
	AR	2	DSP56002 24-Bit Digital Signal Processor User's Manual, Motorola, Inc., 386 pages (1995). (Bates Numbers L07277-L07662).

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	AA3	5,581,773	12/1996	Glover	395	800	
	AB3	5,590,345	12/1996	Barker <i>et al.</i>	395	800	
	AC3	5,666,298	09/1997	Peleg <i>et al.</i>	364	715.08	
	AD3	5,669,010	09/1997	Duluk, Jr.	395	800.22	
	AE3	5,721,892	02/1998	Peleg <i>et al.</i>	395	562	
	AF3	5,734,874	03/1998	Van Hook <i>et al.</i>	395	513	
	AG3	5,740,340	04/1998	Purcell <i>et al.</i>	395	118	
	AH3	5,752,071	05/1998	Tubbs <i>et al.</i>	395	800.34	
	AI3	5,758,176	05/1998	Agarwal <i>et al.</i>	395	800	

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	AJ3						Yes No
	AK3						Yes No
	AL3						Yes No
	AM3						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AN	3	Peleg, A. and Weiser, U., "MMX Technology Extension to the Intel Architecture," <i>IEEE Micro</i> , IEEE, pp. 42-50 (August 1996).
	AO	3	<i>QML-DSP/MCM and Die Courier</i> , at http://www.ti.com/sc/docs/military/liter/ecour/dsp.htm , Texas Instruments, Inc., 7 pages (January 1996).
	AP	3	Turley, J. and Hakkarainen, H., "TI's New 'C6x DSP Screams at 1,600 MIPS," <i>Microprocessor Report</i> , MicroDesign Resources, pp. 14-17 (February 17, 1997).
	AQ	3	<i>DSP56002 24-Bit Digital Signal Processor Semiconductor Technical Data</i> , Motorola, Inc., 110 pages (1996). (Bates Numbers L07803-L07912).
	AR	3	<i>A236 Parallel Digital Signal Processor Chip Programmer's Reference Manual</i> , Oxford Micro Devices, Inc., 195 pages (1994). (Bates Numbers L11184-L11378).

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	AA4	5,761,523	06/1998	Wilkinson <i>et al.</i>	395	800.2	
	AB4	5,778,241	07/1998	Bindloss <i>et al.</i>	395	800.2	
	AC4	5,784,602	07/1998	Glass <i>et al.</i>	395	561	
	AD4	5,793,661	08/1998	Dulong <i>et al.</i>	364	750.5	
	AE4	5,812,147	09/1998	Van Hook <i>et al.</i>	345	511	
	AF4	5,812,723	09/1998	Ohtsu <i>et al.</i>	385	128	
	AG4	5,815,723	09/1998	Wilkinson <i>et al.</i>	395	800.2	
	AH4	5,822,606	10/1998	Morton	395	800.16	
	AI4	5,838,984	11/1998	Nguyen <i>et al.</i>	395	800.05	

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	AJ4						Yes No
	AK4						Yes No
	AL4						Yes No
	AM4						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AN	<u>4</u>	Lee, R., "Accelerating Multimedia with Enhanced Microprocessors," <i>IEEE Micro</i> , IEEE, pp. 22-32 (April 1995). (Bates Numbers L08566-L08576).
	AO	<u>4</u>	DSP56000 24-Bit Digital Signal Processor Family Manual, Motorola, Inc., 638 pages (1995). (Bates Numbers L08722-L09359).
	AP	<u>4</u>	Microprocessor Devices Reliability Monitor Report: Third Quarter 1997, Motorola, 8 pages (1997).
	AQ	<u>4</u>	Reliability Monitor Environmental Stress Data 3Q97, Motorola, 4 pages (1997).
	AR	<u>4</u>	Reliability and Quality Report 3Q97, Motorola, 2 pages (1997).

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA5	5,864,703	01/1999	van Hook <i>et al.</i>	395	800.22
	AB5	5,881,307	03/1999	Park <i>et al.</i>	395	800.23
	AC5	5,936,872	08/1999	Fischer <i>et al.</i>	364	754.03
	AD5	5,960,012	09/1999	Spracklen	371	53
	AE5	5,996,056	11/1999	Volkonsky	712	1
	AF5	6,006,316	12/1999	Dinkjian	712	22
	AG5	6,058,465	05/2000	Nguyen	712	7
	AH5	6,088,783	07/2000	Morton	712	22
	AI5					

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ5					Yes No
	AK5					Yes No
	AL5					Yes No
	AM5					Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

AN	<u>5</u>	Microprocessor Devices Reliability Monitor Report: Fourth Quarter 1997, Motorola, 10 pages (1997).
AO	<u>5</u>	Reliability and Quality Report 4Q97, Motorola, 2 pages (1997).
AP	<u>5</u>	"Hardware Implications of <i>xmem</i> as a <i>st</i> followed by a <i>ld</i> ," Motorola Semiconductor Engineering Bulletin, Motorola, 5 pages (September 1992).
AQ	<u>5</u>	601 First Silicon, at http://www.mot.com/SPS/PowerPC/library/press_releases/601_First_Silicon.html , Motorola, 2 Pages (October 1, 1992).
AR	<u>5</u>	DSP56002/DSP56L002 24-Bit Digital Signal Processor Motorola Semiconductor Product Information, Motorola, Inc., 3 pages (1994). (Bates Numbers L07913-L07915).

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	AA6						
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	AG6						
	AH6						
	AI6						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ6						Yes No
	AK6						Yes No
	AL6						Yes No
	AM6						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AN	<u>6</u>	MC88410UM/AD: MC88410 Secondary Cache Controller User's Manual, at http://www.mot-sps.com/lit/html/mc88410umad.html , Motorola, Inc., 1 page (January 24, 1997).
	AO	<u>6</u>	AN1214: MC88110 64-bit External Bus Interface to 16-bit EPROM, at http://www.mot-sps.com/lit/html/an1214.html , Motorola, Inc., 1 page (January 24, 1997).
	AP	<u>6</u>	TMS320C80 Digital Signal Processor, Texas Instruments Corp., p. 41 (July 1994).
	AQ	<u>6</u>	EB162: Programming Tips (MC88110), at http://www.mot-sps.com/lit/html/eb162.html , Motorola, Inc., 1 page (January 24, 1997).
	AR	<u>6</u>	MC88110/410DH/AD: MC88110/MC88410 Designer's Handbook, at http://www.mot-sps.com/lit/html/mc88110410dhad.html , Motorola, Inc., 1 page (January 24, 1997).

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	AA7						
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	AH7						
	AI7						

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	AJ7						Yes No
	AK7						Yes No
	AL7						Yes No
	AM7						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AN	<u>z</u>	MC88110UMAD: MC88110 Second Generation RISC Microprocessor User's Manual, at http://www.mot-sps.com/lit/html/mc88110umad.html , Motorola, Inc., 1 page (January 24, 1997).
	AO	<u>z</u>	Weiss, R., ARM Piccolo Mixes ARM RISC with DSP, at http://www.estd.com/Editorial/1996/11/Briefs/arm.html , 1 page, (November 1996).
	AP	<u>z</u>	TMS320C1x/C2x/C2xx/C5x Assembly Language Tools User's Guide, Texas Instruments, 483 pages (March 1995). (Bates Numbers L07916-L08398).
	AQ	<u>z</u>	TMS320C5x General-Purpose Applications User's Guide, Texas Instruments, 167 pages (July 1997). (Bates Numbers L08399-L08565).
	AR	<u>z</u>	Ei-Sharkawy, Mohamed, Ph.D., Digital Signal Processing Applications with Motorola's DSP56002 Processor, Prentice Hall PTR, pp. 43-124 (1996). (Bates Numbers L06519-L06601).

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	AA8						
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	AI8						

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	AJ8						Yes No
	AK8						Yes No
	AL8						Yes No
	AM8						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AN	8	Killian, E., "MIPS Extensions for Digital Media," <i>Microprocessor Forum</i> , pp. 1- 5 (October 22-23, 1996).
	AO	8	"Interrupt Latency in the MC88110," <i>Motorola Semiconductor Engineering Bulletin</i> , Motorola, pp. 1, 2 and 4-9 (1992).
	AP	8	"Running the MC88110 in Lockstep," <i>Motorola Semiconductor Engineering Bulletin</i> , Motorola, 2 pages (1992).
	AQ	8	Turley, J., "Siemens TriCore Revives CISC Techniques: New 32-Bit Design Emphasizes DSP Capability and Microcontroller Functions," <i>Microprocessor Report</i> , Microdesign Resources, pp. 13-16 (November 17, 1997).
	AR	8	<i>i860™ Microprocessor Family Programmer's Reference Manual</i> , Intel Corporation, 79 pages (1992). (Bates Numbers L09361-L09439).

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	AD2	4,583,199	04/1986	Boothroyd <i>et al.</i>	364	900	
	AE2	4,773,006	09/1988	Kinoshita <i>et al.</i>	364	200	
	AF2	4,814,976	03/1989	Hansen <i>et al.</i>	364	200	
	AG2	5,150,290	09/1992	Hunt	364	736	
	AH2	5,418,915	05/1995	Matuda <i>et al.</i>	395	325	
	AI2	5,513,366	04/1996	Agarwal <i>et al.</i>	395	800	

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION
	AJ2						Yes No
	AK2						Yes No
	AL2						Yes No
	AM2						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AN	2	Fiegel, C.P., "TI Introduces Four-Processor DSP Chip," <i>Microprocessor Report</i> , MicroDesign Resources, pp. 22-25 (March 28, 1994).
	AO	2	Gwennap, L., "Digital, MIPS Add Multimedia Extensions," <i>Microprocessor Report</i> , MicroDesign Resources, Vol. 10, No. 15, pp. 1-5 (November 18, 1996).
	AP	2	Gwennap, L., "New Multimedia Chips to Enter the Fray," <i>Microprocessor Report</i> , MicroDesign Resources, Vol. 10, No. 13, 1 page (October 1996).
	AQ	2	Hansen, C., "Architecture of a Broadband Mediaprocessor," <i>COMPCON 96</i> , IEE, pp. 1-8 (February 25-29, 1996).
	AR	2	DSP56002 24-Bit Digital Signal Processor User's Manual, Motorola, Inc., 386 pages (1995). (Bates Numbers L07277-L07662).

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	AA3	5,581,773	12/1996	Glover	395	800	
	AB3	5,590,345	12/1996	Barker <i>et al.</i>	395	800	
	AC3	5,666,298	09/1997	Peleg <i>et al.</i>	364	715.08	
	AD3	5,669,010	09/1997	Duluk, Jr.	395	800.22	
	AE3	5,721,892	02/1998	Peleg <i>et al.</i>	395	562	
	AF3	5,734,874	03/1998	Van Hook <i>et al.</i>	395	513	
	AG3	5,740,340	04/1998	Purcell <i>et al.</i>	395	118	
	AH3	5,752,071	05/1998	Tubbs <i>et al.</i>	395	800.34	
	AI3	5,758,176	05/1998	Agarwal <i>et al.</i>	395	800	

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	AJ3						Yes No
	AK3						Yes No
	AL3						Yes No
	AM3						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AN	<u>3</u>	Peleg, A. and Weiser, U., "MMX Technology Extension to the Intel Architecture," <i>IEEE Micro</i> , IEEE, pp. 42-50 (August 1996).
	AO	<u>3</u>	QML-DSP/MCM and Die Courier, at http://www.ti.com/sc/docs/military/liter/ecour/dsp.htm , Texas Instruments, Inc., 7 pages (January 1996).
	AP	<u>3</u>	Turley, J. and Hakkarainen, H., "TI's New 'C6x DSP Screams at 1,600 MIPS," <i>Microprocessor Report</i> , MicroDesign Resources, pp. 14-17 (February 17, 1997).
	AQ	<u>3</u>	DSP56002 24-Bit Digital Signal Processor Semiconductor Technical Data, Motorola, Inc., 110 pages (1996). (Bates Numbers L07803-L07912).
	AR	<u>3</u>	A236 Parallel Digital Signal Processor Chip Programmer's Reference Manual, Oxford Micro Devices, Inc., 195 pages (1994). (Bates Numbers L11184-L11378).

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	AA4	5,761,523	06/1998	Wilkinson <i>et al.</i>	395	800.2	
	AB4	5,778,241	07/1998	Bindloss <i>et al.</i>	395	800.2	
	AC4	5,784,602	07/1998	Glass <i>et al.</i>	395	561	
	AD4	5,793,661	08/1998	Dulong <i>et al.</i>	364	750.5	
	AE4	5,812,147	09/1998	Van Hook <i>et al.</i>	345	511	
	AF4	5,812,723	09/1998	Ohtsu <i>et al.</i>	385	128	
	AG4	5,815,723	09/1998	Wilkinson <i>et al.</i>	395	800.2	
	AH4	5,822,606	10/1998	Morton	395	800.16	
	AI4	5,838,984	11/1998	Nguyen <i>et al.</i>	395	800.05	

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	AJ4						Yes No
	AK4						Yes No
	AL4						Yes No
	AM4						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AN	<u>4</u>	Lee, R., "Accelerating Multimedia with Enhanced Microprocessors," <i>IEEE Micro</i> , IEEE, pp. 22-32 (April 1995). (Bates Numbers L08566-L08576).
	AO	<u>4</u>	DSP56000 24-Bit Digital Signal Processor Family Manual, Motorola, Inc., 638 pages (1995). (Bates Numbers L08722-L09359).
	AP	<u>4</u>	Microprocessor Devices Reliability Monitor Report: Third Quarter 1997, Motorola, 8 pages (1997).
	AQ	<u>4</u>	Reliability Monitor Environmental Stress Data 3Q97, Motorola, 4 pages (1997).
	AR	<u>4</u>	Reliability and Quality Report 3Q97, Motorola, 2 pages (1997).

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	AA5	5,864,703	01/1999	van Hook et al.	395	800.22	
	AB5	5,881,307	03/1999	Park et al.	395	800.23	
	AC5	5,936,872	08/1999	Fischer et al.	364	754.03	
	AD5	5,960,012	09/1999	Spracklen	371	53	
	AE5	5,996,056	11/1999	Volkonsky	712	1	
	AF5	6,006,316	12/1999	Dinkjian	712	22	
	AG5	6,058,465	05/2000	Nguyen	712	7	
	AH5	6,088,783	07/2000	Morton	712	22	
	AI5						

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	AJ5						Yes No
	AK5						Yes No
	AL5						Yes No
	AM5						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AN	<u>5</u>	Microprocessor Devices Reliability Monitor Report: Fourth Quarter 1997, Motorola, 10 pages (1997).
	AO	<u>5</u>	Reliability and Quality Report 4Q97, Motorola, 2 pages (1997).
	AP	<u>5</u>	"Hardware Implications of xmem as a st followed by a ld," Motorola Semiconductor Engineering Bulletin, Motorola, 5 pages (September 1992).
	AQ	<u>5</u>	601 First Silicon, at http://www.mot.com/SPS/PowerPC/library/press_releases/601_First_Silicon.html , Motorola, 2 Pages (October 1, 1992).
	AR	<u>5</u>	DSP56002/DSP56L002 24-Bit Digital Signal Processor Motorola Semiconductor Product Information, Motorola, Inc., 3 pages (1994). (Bates Numbers L07913-L07915).

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	AA6						
	AB6						
	AC6						
	AD6						
	AE6						
	AF6						
	AG6						
	AH6						
	AI6						

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	AJ6						Yes No
	AK6						Yes No
	AL6						Yes No
	AM6						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AN	<u>6</u>	MC88410UM/AD: MC88410 Secondary Cache Controller User's Manual, at http://www.mot-sps.com/lit/html/mc88410umad.html , Motorola, Inc., 1 page (January 24, 1997).
	AO	<u>6</u>	AN1214: MC88110 64-bit External Bus Interface to 16-bit EPROM, at http://www.mot-sps.com/lit/html/an1214.html , Motorola, Inc., 1 page (January 24, 1997).
	AP	<u>6</u>	TMS320C80 Digital Signal Processor, Texas Instruments Corp., p. 41 (July 1994).
	AQ	<u>6</u>	EB162: Programming Tips (MC88110), at http://www.mot-sps.com/lit/html/eb162.html , Motorola, Inc., 1 page (January 24, 1997).
	AR	<u>6</u>	MC88110/410DH/AD: MC88110/MC88410 Designer's Handbook, at http://www.mot-sps.com/lit/html/mc88110410dhad.html , Motorola, Inc., 1 page (January 24, 1997).

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	AA7						
	AB7						
	AC7						
	AD7						
	AE7						
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	AG7						
	AH7						
	AI7						

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	AJ7						Yes No
	AK7						Yes No
	AL7						Yes No
	AM7						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AN	<u>1</u>	MC88110UMAD: MC88110 Second Generation RISC Microprocessor User's Manual, at http://www.mot-sps.com/lit/html/mc88110umad.html , Motorola, Inc., 1 page (January 24, 1997).
	AO	<u>1</u>	Weiss, R., ARM Piccolo Mixes ARM RISC with DSP, at http://www.estd.com/Editorial/1996/11/Briefs/arm.html , 1 page, (November 1996).
	AP	<u>1</u>	TMS320C1x/C2x/C2xx/C5x Assembly Language Tools User's Guide, Texas Instruments, 483 pages (March 1995). (Bates Numbers L07916-L08398).
	AQ	<u>1</u>	TMS320C5x General-Purpose Applications User's Guide, Texas Instruments, 167 pages (July 1997). (Bates Numbers L08399-L08565).
	AR	<u>1</u>	El-Sharkawy, Mohamed, Ph.D., Digital Signal Processing Applications with Motorola's DSP56002 Processor, Prentice Hall PTR, pp. 43-124 (1996). (Bates Numbers L06519-L06601).

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	AA8						
	AB8						
	AC8						
	AD8						
	AE8						
	AF8						
	AG8						
	AH8						
	AI8						

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION
	AJ8						Yes No
	AK8						Yes No
	AL8						Yes No
	AM8						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AN	<u>8</u>	Killian, E., "MIPS Extensions for Digital Media," <i>Microprocessor Forum</i> , pp. 1- 5 (October 22-23, 1996).
	AO	<u>8</u>	"Interrupt Latency in the MC88110," <i>Motorola Semiconductor Engineering Bulletin</i> , Motorola, pp. 1, 2 and 4-9 (1992).
	AP	<u>8</u>	"Running the MC88110 in Lockstep," <i>Motorola Semiconductor Engineering Bulletin</i> , Motorola, 2 pages (1992).
	AQ	<u>8</u>	Turley, J., "Siemens TriCore Revives CISC Techniques: New 32-Bit Design Emphasizes DSP Capability and Microcontroller Functions," <i>Microprocessor Report</i> , Microdesign Resources, pp. 13-16 (November 17, 1997).
	AR	<u>8</u>	<i>i860™ Microprocessor Family Programmer's Reference Manual</i> , Intel Corporation, 79 pages (1992). (Bates Numbers L09361-L09439).

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